

Form 1449*	Atty. Docket No.: 884.107US1	Serial No. 09/347,690
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		Applicant: Manpreet S. Khaira et al.
	Filing Date: July 2, 1999	Group: 2755

U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if appropriate
<i>SL</i>	5,469,366	11/21/1995	Yang, D., et al.	364	409	09/20/93

FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Explanation Yes No
(Including Author, Title, Date, Pertinent Pages, Etc.)						

OTHER DOCUMENTS

<i>SL</i>	Bryant, R.E., et al., "COSMOS: A Compiled Simulator for MOS Circuits", <u>Proceedings of the 24th ACM/IEEE Design Automation Conference</u> , Miami Beach, FL, pp. 9-16, (1987)
<i>SL</i>	Casas, J., et al., "Logic Verification of Very Large Circuits Using Shark", <u>Proceedings of the Twelfth International Conference on VLSI Design</u> , Goa, India, pp. 310-317, (Jan. 7-10, 1999)
<i>SL</i>	Manjikian, N., et al., "High Performance Parallel Logic Simulation on a Network of Workstations", <u>Proceedings of the 7th Workshop on Parallel and Distributed Simulation (PADS)</u> , San Diego, CA, pp. 76-84, (May 16-19, 1993)



Examiner <i>SL</i>	Date Considered <i>8/12/02</i>
--------------------	--------------------------------

*Substitute Disclosure Statement Form (PTO-1449)

**EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.